## **ELECTRONIC INFORMATION DISCLOSURE STATEMENT**

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

FINFET WITH LOW GATE CAPACITANCE AND LOW EXTRINSIC RESISTANCE

Application Number :

Confirmation Number:

First Named Applicant: Brent Anderson
Attorney Docket Number: BUR920040040US1

Art Unit: Examiner:

Search string: (6660596 or 6635923 or 6118161 or 20030178670 or 20030151077 ).pn

## **US Patent Documents**

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6660596	2003-12-09	Adkisson, et al			
	2	6635923	2003-10-21	Hanafi, et al			
	3	6118161	2000-09-12	Chapman, et al			

## **US Published Applications**

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20030178670	2003-09-25	Fried, et al			
	2	20030151077	2003-08-14	Mathew, et al			

## **Signature**

Examiner Name	Date		